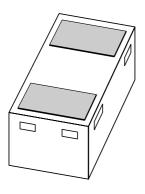
DISCRETE SEMICONDUCTORS

DATA SHEET



BAS70LSchottky barrier diode

Product specification

2003 May 20





Schottky barrier diode

BAS70L

FEATURES

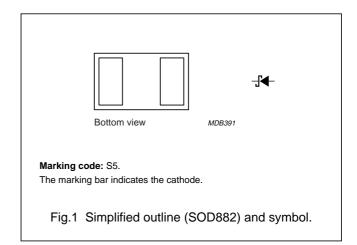
- · Low diode capacitance
- · Low forward voltage
- · Guard ring protected
- · High breakdown voltage
- Leadless ultra small plastic package (1 mm \times 0.6 mm \times 0.5 mm)
- Boardspace 1.17 mm² (approx. 10% of SOT23)
- Power dissipation comparable to SOT23.

APPLICATIONS

- Ultra high-speed switching
- · Voltage clamping
- · Protection circuits
- Mobile communication, digital (still) cameras, PDAs and PCMCIA cards.

DESCRIPTION

Planar Schottky barrier diode with an integrated guard ring for stress protection. Encapsulated in a SOD882 leadless ultra small plastic package.



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_R	continuous reverse voltage		_	70	V
I _F	continuous forward current		_	70	mA
I _{FRM}	repetitive peak forward current	$t_p \le 1 \text{ s}; \ \delta \le 0.5$	_	70	mA
I _{FSM}	non-repetitive peak forward current	t _p < 10 ms	_	100	mA
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C

Philips Semiconductors Product specification

Schottky barrier diode

BAS70L

ELECTRICAL CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
V _F	forward voltage	see Fig.2		
		I _F = 1 mA	410	mV
		I _F = 10 mA	750	mV
		I _F = 15 mA	1	V
I _R	continuous reverse current	V _R = 50 V; see Fig.3; note 1	100	μΑ
		V _R = 70 V; see Fig.3; note 1	10	μΑ
C _d	diode capacitance	V _R = 0 V; f = 1 MHz; see Fig.5	2	pF

Note

1. Pulse test: pulse width = 300 μ s; δ = 0.02.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Refer to SOD882 standard mounting conditions (footprint), FR4 with 60 μm copper strip line.

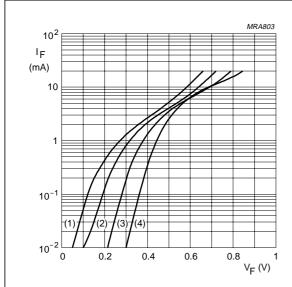
Soldering

Reflow soldering is the only recommended soldering method.

Schottky barrier diode

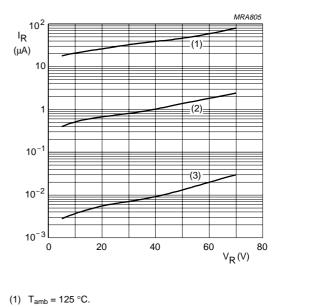
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GRAPHICAL DATA



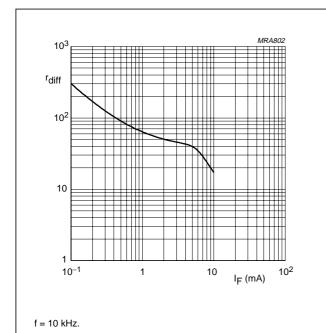
- (1) $T_{amb} = 125 \,^{\circ}C$.
- (2) $T_{amb} = 85 \, ^{\circ}C$.
- (3) $T_{amb} = 25 \, ^{\circ}C$.
- (4) $T_{amb} = -40 \, ^{\circ}C$.

Fig.2 Forward current as a function of forward voltage; typical values.



- (2) $T_{amb} = 85 \, ^{\circ}C$.
- (3) $T_{amb} = 25 \,^{\circ}C$.

Reverse current as a function of reverse voltage; typical values.



Differential forward resistance as a function Fig.4 of forward current; typical values.

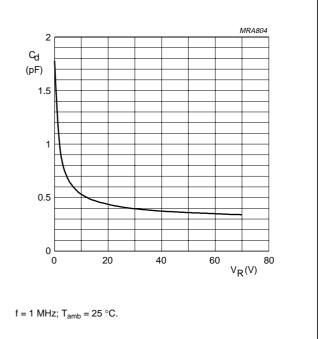


Fig.5 Diode capacitance as a function of reverse voltage; typical values.

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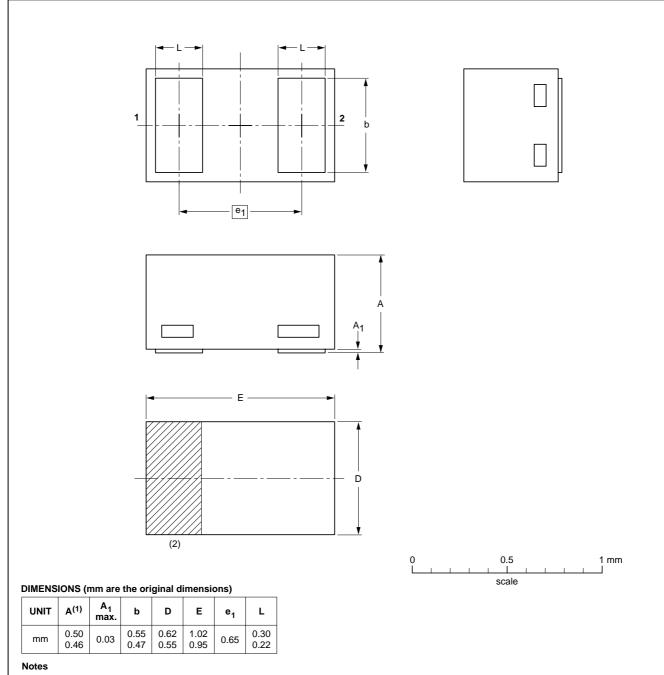
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PACKAGE OUTLINE

Leadless ultra small plastic package; 2 terminals; body 1.0 x 0.6 x 0.5 mm

SOD882



- 1. Including plating thickness
- 2. The marking bar indicates the cathode

OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOD882						03-04-16 03-04-17

2003 May 20 5 Philips Semiconductors Product specification

Schottky barrier diode

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DATA SHEET STATUS

LEVEL	DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
III	Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Relevant changes will be communicated via a Customer Product/Process Change Notification (CPCN).

Notes

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